

Sub B3  
12. A circuit as claimed in claim 2, wherein the bus structure is provided with a number of registers or other connections and wherein these connections are programmable from an instruction register.

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13. A circuit as claimed in claim 3, wherein the bus structure is provided with a number of registers or other connections and wherein these connections are programmable from an instruction register.

14. A circuit as claimed in claim 7, wherein the bus structure is provided with a number of registers or other connections and wherein these connections are programmable from an instruction register.--

#### REMARKS

This Preliminary Amendment is submitted to change the multiple dependent claims to single dependent claims and to reduce the government filing fee.

#### EXPRESS MAIL CERTIFICATE

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail Post Office to Addressee (mail label # RB885680257US) in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on April 14, 1995:

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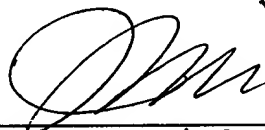
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JAF/jc

Respectfully submitted,



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